



\$ case

THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent No6,713,346 B2
Patent Issue Date March 30, 2004
Application Serial No.09/768,878
Filing Date January 23, 2001
Assignee Micron Technology, Inc.
Inventorship Graham Wolstenholme
Attorney's Docket No.MI55-003
Title: Methods of Forming a Line of FLASH Memory Cells

**REQUEST FOR CERTIFICATE OF CORRECTION OF PATENT FOR
APPLICANT MISTAKE and PTO MISTAKES (37 C.F.R. §§ 1.322(a) and 1.323)**

To:	Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450	Certificate AUG 18 2004
ATTN:	Decision and Certificate of Correction Branch of the Patent Issue Division	of Correction
From:	Mark S. Matkin (Tel. 509-624-4276; Fax 509-838-3424) Wells St. John P.S. 601 W. First Avenue, Suite 1300 Spokane, WA 99201-3828	

Sir:

It is hereby requested that a Certificate of Correction be issued with respect to Patent No. 6,713,346 B2, granted March 30, 2004, in accordance with the Certificate of Correction form attached hereto in duplicate.

It is noted that an error appears in this patent of a typographical nature of character, as more fully described below. The error occurred in good faith. Correction thereof does not involve such changes in the patent as would constitute new matter or would require re-examination.

08/17/2004 SHASSEN2 00000090 6713346

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100.00 0P

AUG 19 2004

The exact page and line number where the error occurs in the application file is:

Page 6, line 18 in the specification; Col. 3, line 31 in the issued patent;

Page 33, line 3 in the specification; Abstract, line 2 in the issued patent.

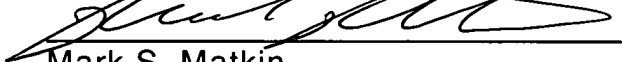
Other errors listed on the Certificate of Correction form were apparently incurred through the fault of the PTO as will be disclosed by the records of files in the Office.

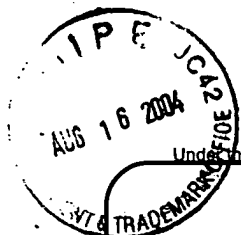
Attached hereto, in duplicate, is Form PTO-1050, with at least one copy being suitable for printing.

Enclosed is a check in the amount of \$100.00, as required by 37 CFR 1.20(a).

Respectfully submitted,

Dated: 8-12-04

By: 
Mark S. Matkin
Reg. No. 32,268



TRANSMITTAL FORM

(to be used for all correspondence after initial filing)

Total Number of Pages in This Submission

9

Application Number

09/768,878

Filing Date

January 23, 2001

First Named Inventor

Graham Wolstenholme

Art Unit

2812

Examiner Name

Richard A. Booth

Attorney Docket Number

MI55-003

ENCLOSURES (Check all that apply)



Fee Transmittal Form



Fee Attached
Chk No. 143724
Amendment/Reply



After Final



Affidavits/declaration(s)



Extension of Time Request



Express Abandonment Request



Information Disclosure Statement



Certified Copy of Priority
Document(s)



Response to Missing Parts/
Incomplete Application



Response to Missing Parts
under 37 CFR 1.52 or 1.53



Drawing(s)



Licensing-related Papers



Petition



Petition to Convert to a
Provisional Application



Power of Attorney, Revocation



Change of Correspondence Address



Terminal Disclaimer



Request for Refund



CD, Number of CD(s) _____



After Allowance communication
to Technology Center (TC)



Appeal Communication to Board
of Appeals and Interferences
Appeal Communication to TC
(Appeal Notice, Brief, Reply Brief)



Proprietary Information



Status Letter



Other Enclosure(s) (please
Identify below):

PTO Return Receipt Postcard
Req for Cert of Correction
Cert of Correction in duplicate

Remarks

Customer No. 021567

U.S. Patent No. 6,713,346 B2
Issued March 30, 2004

SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

Firm
or
Individual name

Mark S. Matkin, Reg. No. 32,268
Wells St. John P.S.

Signature

Date

8/12/04

CERTIFICATE OF TRANSMISSION/MAILING

I hereby certify that this correspondence is being facsimile transmitted to the USPTO or deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on the date shown below.

Typed or printed name

Muriel G. Dunnigan

Signature

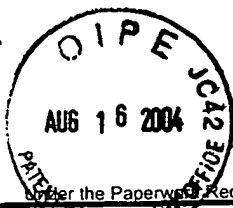
Muriel G. Dunnigan

Date

August 12, 20024

This collection of information is required by 37 CFR 1.5. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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FEE TRANSMITTAL for FY 2004

Effective 10/01/2003, Patent fees are subject to annual revision.

☐ Applicant claims small entity status. See 37 CFR 1.27

TOTAL AMOUNT OF PAYMENT (\$)**100.00**

Complete if Known

Application Number	09/768,878
Filing Date	January 23, 2001
First Named Inventor	Graham Wolstenholme
Examiner Name	Richard A. Booth
Art Unit	2812
Attorney Docket No.	MI55-003

METHOD OF PAYMENT (check all that apply)

☒ Check ☐ Credit card ☐ Money Order ☐ Other ☐ None

☒ Deposit Account:

Deposit
Account
Number
Deposit
Account
Name

23-0925

Wells St. John P.S.

The Director is authorized to: (check all that apply)

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☒ Charge any additional fee(s) or any underpayment of fee(s)

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FEE CALCULATION

1. BASIC FILING FEE

Large Entity		Small Entity		Fee Description	Fee Paid
Fee Code	Fee (\$)	Fee Code	Fee (\$)		
1001	770	2001	385	Utility filing fee	
1002	340	2002	170	Design filing fee	
1003	530	2003	265	Plant filing fee	
1004	770	2004	385	Reissue filing fee	
1005	160	2005	80	Provisional filing fee	

SUBTOTAL (1) (\$)**- 0 -**

2. EXTRA CLAIM FEES FOR UTILITY AND REISSUE

Total Claims		Extra Claims		Fee from below		Fee Paid	
Independent	Multiple Dependent	-20**	-3**	X		X	

Large Entity		Small Entity		Fee Description
Fee Code	Fee (\$)	Fee Code	Fee (\$)	
1202	18	2202	9	Claims in excess of 20
1201	86	2201	43	Independent claims in excess of 3
1203	290	2203	145	Multiple dependent claim, if not paid
1204	86	2204	43	** Reissue independent claims over original patent
1205	18	2205	9	** Reissue claims in excess of 20 and over original patent

SUBTOTAL (2) (\$)**- 0 -**

**or number previously paid, if greater; For Reissues, see above

FEE CALCULATION (continued)

3. ADDITIONAL FEES

Large Entity Small Entity

Fee Code	Fee (\$)	Fee Code	Fee (\$)	Fee Description	Fee Paid
1051	130	2051	65	Surcharge - late filing fee or oath	
1052	50	2052	25	Surcharge - late provisional filing fee or cover sheet	
1053	130	1053	130	Non-English specification	
1812	2,520	1812	2,520	For filing a request for ex parte reexamination	
1804	920*	1804	920*	Requesting publication of SIR prior to Examiner action	
1805	1,840*	1805	1,840*	Requesting publication of SIR after Examiner action	
1251	110	2251	55	Extension for reply within first month	
1252	420	2252	210	Extension for reply within second month	
1253	950	2253	475	Extension for reply within third month	
1254	1,480	2254	740	Extension for reply within fourth month	
1255	2,010	2255	1,005	Extension for reply within fifth month	
1401	330	2401	165	Notice of Appeal	
1402	330	2402	165	Filing a brief in support of an appeal	
1403	290	2403	145	Request for oral hearing	
1451	1,510	1451	1,510	Petition to institute a public use proceeding	
1452	110	2452	55	Petition to revive - unavoidable	
1453	1,330	2453	665	Petition to revive - unintentional	
1501	1,330	2501	665	Utility issue fee (or reissue)	
1502	480	2502	240	Design issue fee	
1503	640	2503	320	Plant issue fee	
1460	130	1460	130	Petitions to the Commissioner	
1807	50	1807	50	Processing fee under 37 CFR 1.17(q)	
1806	180	1806	180	Submission of Information Disclosure Stmt	
8021	40	8021	40	Recording each patent assignment per property (times number of properties)	
1809	770	2809	385	Filing a submission after final rejection (37 CFR 1.129(a))	
1810	770	2810	385	For each additional invention to be examined (37 CFR 1.129(b))	
1801	770	2801	385	Request for Continued Examination (RCE)	
1802	900	1802	900	Request for expedited examination of a design application	

Other fee (specify) **Req for Cert of Correction**

*Reduced by Basic Filing Fee Paid

SUBTOTAL (3) (\$)**100.00**

SUBMITTED BY

Name (Print/Type)	Mark S. Matkin	Registration No. (Attorney/Agent)	32,268	Telephone	(509) 624-4276
Signature		Date	8/12/04		

WARNING: Information on this form may become public. Credit card information should not be included on this form. Provide credit card information and authorization on PTO-2038.

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PATENT & TRADEMARK OFFICE

PTO/SB/17 (10-03)

Approved for use through 07/31/2006. OMB 0651-0032
U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number.

FEE TRANSMITTAL for FY 2004

Effective 10/01/2003. Patent fees are subject to annual revision.

☐ Applicant claims small entity status. See 37 CFR 1.27

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Examiner Name **Richard A. Booth**
Art Unit **2812**
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Independent Claims -3** = X =
Multiple Dependent =

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FEE CALCULATION (continued)

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Other fee (specify) Req for Cert of Correction					100.00
*Reduced by Basic Filing Fee Paid					
SUBTOTAL (3)					(\$) 100.00

SUBMITTED BY

Name (Print/Type) **Mark S. Matkin** Registration No. **32,268** Telephone **(509) 624-4276**
Signature *[Signature]* Date **8/12/04**

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UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 6,713,346 B2
DATED : March 30, 2004
INVENTOR(S) : Graham Wolstenholme

It is certified that errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Face Page, (57) Abstract, line 2 –

Replace “forming flash memory. In one implementation, a line of”
With --forming FLASH memory. In one implementation, a line of--

Col. 3, line 31 –

Replace “FIG. 1 is a diagrammatic top plan of a semiconductor”
With -- FIG. 1 is a diagrammatic top plan view of a semiconductor--

Col. 6, line 10 –

Replace “having portions thereof angled at least 150 from a direction”
With -- having portions thereof angled at least 15° from a direction--

Col. 8, line 15 –

Replace “spacer is formed on one of the=source side or the drain side”
With -- spacer is formed on one of the source side or the drain side--

Col. 8, line 57, claim 1 –

Replace “A method at forming a line of FLASH memory cells”
With -- A method of forming a line of FLASH memory cells—

Col. 10, line 31, claim 10 –

Replace “insulative sidewall spacer having en innermost surface”
With -- insulative sidewall spacer having an innermost surface--

Page
1 of 1

Mailing Address of Sender:
Wells St. John P.S.
601 West First Avenue, Suite 1300
Spokane, WA 99201-3828

Patent No. 6,713,346 B2

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

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601 West First Avenue, Suite 1300
Spokane, WA 99201-3828

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